

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Stephen R. Van Doren, et al.

Confirmation No.:

Application No.: Unknown

Examiner: Unknown

Filing Date: Herewith

Group Art Unit:

Title: TRANSACTION REFERENCES FOR REQUESTS IN A MULTI-PROCESSOR NETWORK

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☒ under 37 CFR 1.97(b), or
(Within three months of filing national application; or date of entry of national application; or before mailing date of first office action on the merits; whichever occurs last)
- ☐ under 37 CFR 1.97(c) together with either a:
☐ Statement under 37 CFR 1.97(e), or
☐ a \$180.00 fee under 37 CFR 1.17(p), or
(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)
- ☐ under 37 CFR 1.97 (d) together with a:
☐ Statement under 37 CFR 1.97(e)(1) or (2), and
☐ a \$180.00 fee set forth in 37 CFR 1.17(p).
(Filed after final action, a notice of allowance, on or before payment of the issue fee)

Please charge to Deposit Account **08-2025** the sum of \$0.00. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account **08-2025** pursuant to 37 CFR 1.25.

☒ Applicant(s) submit herewith Form PTO 1449 - Information Disclosure Statement together with any required copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

☐ A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO Form 1449, as presently understood by the individuals(s) designated in 37 CFR 1.56 (c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

It is requested that the information disclosed herein be made of record in this application.

"Express Mail" label no. **EU516995842US**

Date of Deposit 1-15-04

I hereby certify that this is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: Commissioner for Patents, Alexandria, VA 22313-1450.

By

Typed Name: **Lisa D. Jones**

Respectfully submitted,

Stephen R. Van Doren, et al

By

Gary J. Pitzer

Attorney/Agent for Applicant(s)

Reg. No. **39,334**

Date: **1-15-04**

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO. 200313750-1	APPLICATION NO.	CONFIRMATION NO.
	APPLICANT Stephen R. Van Doren, et al.		
	FILING DATE herewith	GROUP	

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
/RD/	1A	2001/0034815	10/25/2001	Dungan, et al.	
	1B	2002/0009095	01/24/2002	Van Doren, et al.	
	1C	2002/0073071	06/13/2002	Pong, et al.	
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	1J	2003/0195939	10/16/2003	Edirisooriya, et al.	
	1K	2003/0200397	10/23/2003	McAllister, et al.	

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	1L					
	1M					
	1N					
	1O					
	1P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

/RD/	1Q	RAJEEV, JOSHI, et al., "Checking Cache-Coherence Protocols with TLA+", Kluwer Academic Publishers, 2003, pp. 1-8
	1R	MARTIN, MILO M.K., et al., "Token Coheence: Decoupling Performance and Correctness", ISCA-30, pp. 1-12, June 9-11, 2003
	1S	ACACIO, MANUEL E., et al., "Owner Prediction for Accelerating Cache-to-Cache Transfer Misses in a cc-NUMA Architecture", IEEE 2002

EXAMINER
/Ryan Dare/

DATE CONSIDERED
05/26/2008

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	2B	5,721,855	02/24/1998	Hinton, et al.	
	2C	5,802,577	09/01/1998	Bhat, et al.	
	2D	5,829,040	10/27/1998	Son	
	2E	5,875,467	02/23/1999	Merchant	
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	2H	6,055,605	04/25/2000	Sharma, et al.	
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	2J	6,108,737	08/22/2000	Sharma, et al.	
	2K	6,279,084 B1	08/21/2001	Van Doren, et al.	

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2Q	GHARACHORLOO, KOUROSH, et al., "Architecture and Design of AlphaServer GS320", Western Research Laboratory, (Date Unknown)
2R	GHARACHORLOO, KOUROSH, et al., "Memory Consistency and Event Ordering In Scalable Shared-Memory Multiprocessors", Computer Systems Laboratory, pp. 1-14, (Date Unknown)
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FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR
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(Use several sheets if necessary)

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	3C	6,490,661 B1	12/03/2002	Keller, et al.	
	3D	6,631,401 B1	10/07/2003	Keller, et al.	
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	3G				
	3H				
	3I				
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	3K				

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	3R	
	3S	

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